

FIG. 1

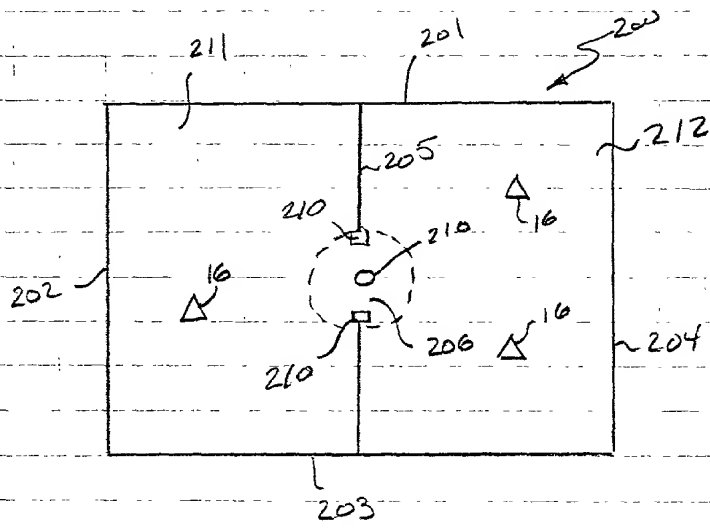


FIGURE 2

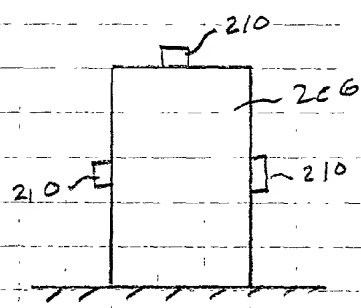


FIGURE 3

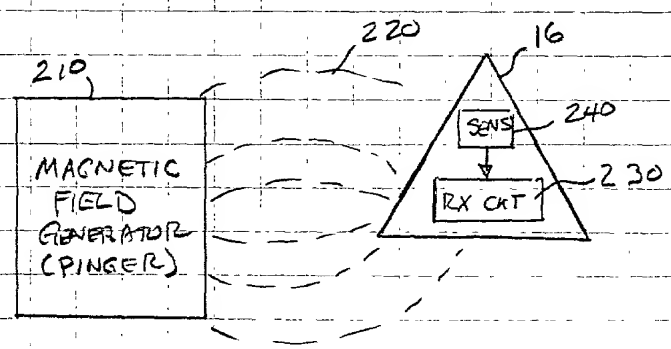


FIGURE 4

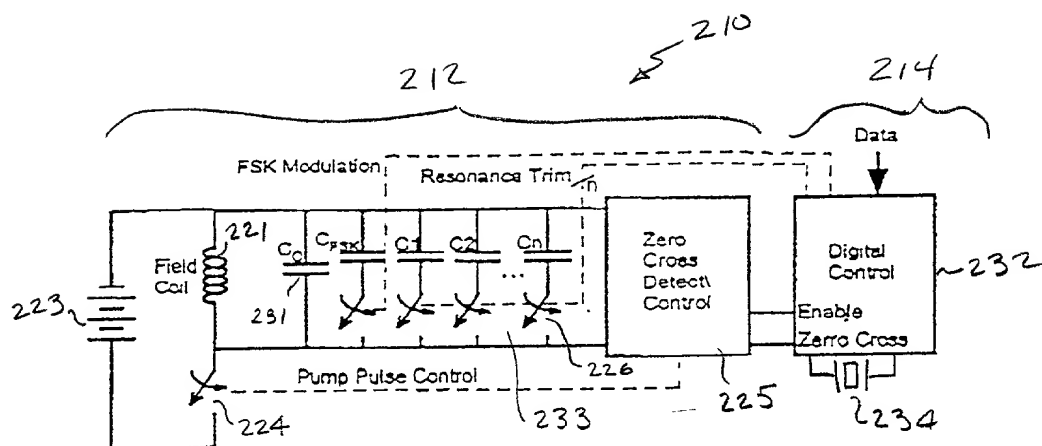


FIGURE 5

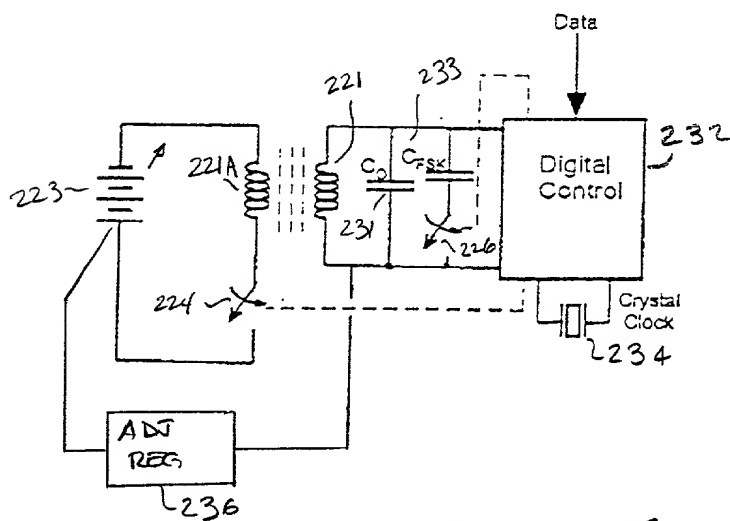


FIGURE 6

The diagram illustrates a digital receiver system. It begins with a Pick-up Coil (301) connected to a matching network (300) which includes a capacitor (302). The output of the matching network is connected to a comparator (305). The comparator is also connected to a Digital Receiver (310). The Digital Receiver is connected to a Digital Demodulator (320). The Digital Receiver also receives a Crystal Clock (306) and outputs a signal (A) to the Digital Demodulator. The Digital Demodulator outputs Data.